

**Electronic Patent Application Fee Transmittal**

<b>Application Number:</b>	10595303			
<b>Filing Date:</b>	19-May-2006			
<b>Title of Invention:</b>	Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers			
<b>First Named Inventor/Applicant Name:</b>	Roy Knechtel			
<b>Filer:</b>	Yisun Song/Robin Taylor			
<b>Attorney Docket Number:</b>	60291.000048			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	150	150

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				150